

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	648	438/259.ccls.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:44
L2	940	438/258.ccls.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:44
L3	2785	438/257.ccls.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:44
L4	75763	Chang.in.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:45
L5	21312	Hsu.in.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:45

EAST Search History

L6	153	1 and flash adj memory and trench with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:45
L7	93	2 and flash adj memory and trench with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:45
L8	427	3 and flash adj memory and trench with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:45
L9	162	4 and flash adj memory and trench with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:45
L10	78	5 and flash adj memory and trench with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:46

EAST Search History

L12	132	1 and flash adj memory and trench with floating adj gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:46
L13	58	2 and flash adj memory and trench with floating adj gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:46
L14	336	3 and flash adj memory and trench with floating adj gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:46
L15	104	4 and flash adj memory and trench with floating adj gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:46
L16	50	5 and flash adj memory and trench with floating adj gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:46

EAST Search History

L17	20	10 and trench with select\$4 with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:47
L18	16	11 and trench with select\$4 with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:47
L19	11	12 and trench with select\$4 with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:47
L20	4	13 and trench with select\$4 with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:48
L21	23	14 and trench with select\$4 with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:49

EAST Search History

L22	23	15 and trench with select\$4 with gate	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:47
L23	9	17 and anisotropic	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:49
L24	7	18 and anisotropic	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:49
L25	3	19 and anisotropic	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:49
L26	0	20 and anisotropic	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:49

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L27	9	21 and anisotropic	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:49
L28	18	22 and anisotropic	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:49
L29	4	23 and conformal with trench	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:50
L30	4	24 and conformal with trench	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:50
L31	3	25 and conformal with trench	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:50

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L32	0	26 and conformal with trench	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:50
L33	0	27 and conformal with trench	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:50
L34	4	28 and conformal with trench	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/12/10 20:50

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	6	"6815758"	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/20 18:55
S2	1	("6137132").PN.	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 18:56
S3	1560	flash adj memory and trench with gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 18:57
S4	814	flash adj memory and trench with floating adj gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 18:57
S5	84	S4 and trench with select\$4 with gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 18:58
S6	54	S5 and (gate adj (insulat\$3 or dielectric or oxide) with trench)	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 18:58
S7	5	S5 and high adj voltage with trench	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 18:59
S8	4	S6 and high adj voltage with trench	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:01
S9	652	second adj etch\$4 with (trench)	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:02
S10	556	S9 and mask	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:02
S11	0	S10 and (floating adj gate with inside with trench)	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:03

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S12	2	S10 and (floating adj gate with in with trench)	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:04
S13	0	S9 and floating adj gate with trench and select with gate with trench	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:04
S14	3864	trench and sidewall with mask\$3	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:04
S15	189	S14 and floating adj gate with trench	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:10
S16	0	S15 and plug with high adj voltage	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:05
S17	8241	trench and sidewall with gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:05
S18	1706	trench with sidewall with gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:05
S19	0	trench with sidewall with floatng adj gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:06
S20	256	trench with sidewall with floating adj gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:06
S21	31	S20 and sidewall with select\$4 with gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:06
S22	30	S21 and etch\$3	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:06
S23	5	S22 and high adj voltage	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:07
S24	0	("2005/0224861").URPN.	USPAT	OR	OFF	2006/08/20 19:09
S25	154	S15 and etch\$4 with trench	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:10

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S26	82	((trench or via or hole or opening) with floating adj gate with select adj gate)	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:11
S27	34	S26 and high adj voltage	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:11
S28	24	S27 and etch\$	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:11
S29	7	("5029130" "6103573" "6329685" "6420231" "6717203" "6747310" "6937514").PN.	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:23
S30	3040	(trench or via or hole or opening) with sidewall with gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:24
S31	94	S30 and second adj (trench or via or hole or opening) with second with sidewall with gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:24
S32	38	S31 and floating adj gate	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:25
S33	36	S31 and floating adj gate with sidewall	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:25
S34	2	S33 and select adj gate with sidewall	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:25
S35	5	("6444525").URPN.	USPAT	OR	OFF	2006/08/20 19:35
S36	6	("20010029077" "5616510" "5773343" "6093606" "6127226" "6239465").PN.	US-PGPU B; USPAT; USOCR	OR	OFF	2006/08/20 19:35